

Curriculum Vitae

Donatella Sciuto
Politecnico di Milano
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Employment History

- 2000 - Full professor of Computer Science and Engineering, Politecnico di Milano, Italy
- 1992 - 2000 Associate professor, Politecnico di Milano
- 1986 - 1992 Assistant professor, Università di Brescia
- 1985 - 1987 Teaching Assistant and Research Assistant, University of Colorado, Boulder, Department of Electrical Engineering and Computer Science
- 1984-1985 Research assistant, Politecnico di Milano

Temporary positions and academic appointments

- 2023 - 2028 Rector of Politecnico di Milano
- 2023 - 2028 Member of the Steering Council of Biblioteca Europea di Informazione e Cultura (BEIC)
- 2023 - 2027 Member of the Supervisory Board of Fondazione Corriere della Sera
- 2023 - 2026 Member of the Supervisory Board of Fondazione TIM
- 2023 - 2024 Member of the Supervisory Board of Fondazione Lombardia per l'Ambiente (FLA)
- 2022 - 2028 Member of the Supervisory Board of the National Research Centre in High-Performance Computing, Big Data e Quantum Computing
- 2022 - 2025 Member of the Scientific-Technical Committee of Agenzia per la Cybersicurezza Nazionale (National Agency for Cybersecurity)
- 2022 - 2025 Member of the Supervisory Board of STMicroelectronics
- 2021 - 2024 Member of the Board of IIT (Italian Institute of Technology)
- 2020 - 2024 Member of the Board of F.I.L.A. SpA
- 2018 - 2022 Member of the Supervisory Board of Human Technopole Foundation (private foundation, founded by the Italian Ministry of Economy and Finance, the Italian Ministry of Health and the Italian Ministry for Education, University and Research)
- 2017 - 2023 Member of the Board of Railway S.p.A, listed company
- 2017 - 2023 Member of the Board of Avio S.p.A., after IPO

- 2016 - 2019 Member of the Board of Directors of ValoreD, Italian association of 200 companies promoting gender balance and inclusive culture in organisations
- 2016 - 2020 International Faculty Affiliate University of Illinois at Chicago, Department of Computer Science
- 2015 - 2018 Member of the Scientific Board of the School of the Security and Intelligence Department of the Italian Government
- 2015 - 2017 Member of the Board of Cineca consortium and of the subsidiary KION S.p.A
- 2013 - Board of Governors (Consiglio Superiore) Bank of Italy, and from 2016 to 2021 Chair of the internal audit committee
- 2011 - 2015 Expert member on industrial research projects for the Ministero dell'Istruzione dell'Università e della Ricerca - Ricerca Industriale
- 2011- 2014 Member of the Scientific Board of Fondazione Ansaldo
- 2010 - 2022 Vice Rector (Prorettore Delegato di Ateneo) of Politecnico di Milano, from 2015 Executive Vice Rector
- 2011 - 2017 Member of the Board of Governors (Consiglio Consortile) of CINECA as Politecnico di Milano representative
- 2005 - 2010 Deputy Director of Education of CEFRIEL
- 2005 - 2011 Head of the Computer Science and Engineering Section of the Dipartimento di Elettronica e Informazione (Department of Electronics and Information), Politecnico di Milano
- 1989 - 2005 Tutor and mentor for the Electronic Design Automation research area and Engineering Master Program of CEFRIEL
- 2000 - 2018 Adjunct Professor University of Illinois Chicago in the Master of Science program in cooperation with Politecnico di Milano
- 1999-2003 Head of the Joint Master of Science program for Politecnico di Milano with University of Illinois Chicago
- 1992-2002 Head of the Computer Science area at the Libera Università Cattaneo, responsible of all computer science related courses for Economics and Engineering Faculties
- 1996-1999 Expert member representing Politecnico di Milano to the Chamber of Commerce on Innovation prize for SMEs
- 1994-1999 Board of Governors of the new Faculty in Logistics Engineering at LIUC Libera Università Carlo Cattaneo

Education

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| 1992 | Master in Business Administration, Scuola di Direzione Aziendale, Bocconi University, Milano, Italy |
| 1988 | PhD in Electrical and Computer Engineering, University of Colorado, Boulder, CO, USA |
| 1984 | Laurea in Ingegneria Elettronica, Politecnico di Milano |

Research interests

She works in the Electronic Design Automation field and in particular, for the past 10 years, on methodologies for embedded systems design, in the design of hardware and software systems and heterogeneous multiprocessor systems. More recently, the research has been addressed also towards more application-oriented topics, from wireless sensor networks, to the design of smart spaces and smart cities, where the embedded systems are one of the main infrastructural components. She is also interested in the area of cybersecurity and fintech, in terms of policies and technologies, with a particular focus on blockchain.

She has managed research projects both at National and European level, and with Italian and international companies. In her career, she has always tutored Master and PhD students who are now holding relevant positions in companies, universities and international research organizations.

Conference Committees (selected)

- Awards Chair of the IEEE/ACM Design Automation and Test in Europe 2017-2022
- Program Co-Chair ACM/IEEE Design Automation Conference 2012 and 2013
- Track Chair ACM/IEEE GLSVLSI 2011
- Track Chair of IEEE ICCD 2013-2017
- Finance Chair of IEEE/ACM DATE 2011- 2014
- Co-General Chair of IEEE/ACM ESWEEK 2010
- Co-General Chair of IEEE/ACM ESWEEK 2009
- General Chair of the IEEE/ACM Design Automation and Test in Europe 2008
- Vice-General Chair of the IEEE/ACM Design Automation and Test in Europe in 2007
- Publications Chair IEEE/ACM ESWEEK 2007
- Member of the executive committee of the IEEE/ACM Design Automation and Test in Europe 1999-2021 in different positions
- Program Chair of the IEEE/ACM Design Automation and Test in Europe in 2006
- Vice Program Chair of the IEEE/ACM Design Automation and Test in Europe in 2005
- Member of the executive committee of IEEE International Conference on CAD as European Representative in 2000-2003
- Program Chairman of the IFIP VHDL Users' Forum in Europe 1998 and 1999
- Program co-chairman of the conference IEEE ISIS 96
- General co-chairman of the conference IEEE ISIS 1997. In those years, she did belong to the Steering Committee
- Member of the Program Committee of the IEEE/ACM Design Automation and Test in Europe since 2003
- Member of the Program Committee IEEE Field Programmable Logic 2019-2021

- Member of the Program Committee of the IEEE CODES+ISSS Conference: 2003-2013
- Member of the Program Committee of the IEEE EMSOFT Conference 2015-2021
- Member of the Program Committee of the IFIP VLSI-Soc Conference 2005-2020
- Member of the Program Committee of the Forum on Design Languages 2004-2006
- Member of the Program Committee of IEEE CASES Conference 2004
- Member of the Program Committee of the Symposium on Integrated Circuits and Systems Design (SBCCI) 2005-2020
- Member of the Program Committee of the IEEE International Conference on Design & Test of Integrated Systems in Nanoscale Technology (DTIS) 2006-2010
- Member of the Program Committee of IEEE PATMOS the International workshop on Power and Timing Modeling, Optimization and Simulation, 2004-2014
- Member of the Program committee of EMSOFT, 2014, 2018-2020
- Member of the SIGDA PhD Forum at DAC Technical Program Committee, 2005-2015
- Member of the Program Committee of the IEEE Great Lakes Symposium 2006-2010.
- Member of the Program Committee of the IEEE/ACM Design Automation Conference 2000-2003
- Program committee member of IEEE Defect and Fault Tolerant Symposium 2003-2014

Editorial Activities

- Associate Editor in Transactions on Emerging Topics in Computing since 2021
- Associate editor of the *IEEE Transactions on Computers* 2016 -2020
- Associate Editor of the IEEE Embedded Systems Letters since 2009.
- Associate Editor of the Journal *Design Automation of Embedded Systems, Kluwer Academic Publishers* 2003-2012
- Associate Editor of the *Journal of System Architectures, North Holland* until 2004
- Associate Editor of the *IEEE Transactions on Computers* from 2003 to 2006.
- Guest Editor of a special issue of IEEE Design&Test of Computers on "design tools for embedded systems", Volume 17, Issue 2, April-June 2000
- Guest Editor of a special issue of the IEEE Transactions on Computer Aided-Design, Volume 26, issue 3
- Guest Editor of a special issue of the EURASIP Journal on Embedded Systems, on Reconfigurable Computing and Hardware/Software Codesign, 2008

Service in professional societies

- Member of the Technical Group IFIP 10.5
- Member of the main board of the European Design and Automation Association from 2005
- Secretary of the European Design and Automation Association from 2009 to 2012

- Vice President of Finance in the IEEE Council of Electronic Design Automation 2008-2009
- Chair of the Awards Committee of the IEEE Council of Electronic Design Automation 2010-2011
- Chair of the IEEE Council of Electronic Design Automation Fellows Committee 2011, 2012, 2014
- Finance Vice President of the IEEE Council of Electronic Design Automation 2008-2009
- President Elect of the IEEE Council of Electronic Design Automation 2010-2011
- President of IEEE Council of Electronic Design Automation 2012-2013
- Member of IEEE Fellows Committee for the Council of Electronic Design Automation, 2013, 2015.
- Member of the IEEE Frederick Philips Award from 2012 to 2016, Chair of the Committee in 2015 and 2016
- Member of the selection committee for the CEDA/EDAC Kaufmann Award 2015-2018
- Member of the selection committee of the EDAA Achievement Award 2016-2017
- Member of the IEEE Technical Activities Board Awards Committee 2015 and 2016
- Member of the IEEE Fellows Board 2017-2019
- Member of the IEEE Noyce medal award committee since 2018, chair of the committee from 2019 to 2021
- Member of the IEEE Wallace McDowell Award since 2020

Recognitions and awards

- 2022 Elected Member of Academia Europaea
- 2021 IBM Women Leaders in AI
- 2019 IEEE CEDA Distinguished Service Award
- She has been nominated Inspiring Fifty Italy in 2018
<https://inspiringfifty.org/inspiring-fifty>
- IEEE Fellow 2011
- European Design Automation Association (EDAA) Fellow 2010
- Outstanding Contribution Award from the IEEE Computer Society in 2009
- European Design and Automation Association Fellowship in 2009
- “Address Bus Encoding Techniques for System-Level Power Optimization”, by L. Benini, G. De Micheli, E. Macii, D. Sciuto, C. Silvano, **DATE-98** has been recognized as one of **the most influential papers** of the past ten years DATE and then selected to be re-published as a chapter of the volume: **Design, Automation, and Test in Europe - The Most Influential Papers of 10 Years DATE** Lauwereins, Rudy; Madsen, Jan (Eds.), 2008.
- Certificate of Appreciation IEEE Computer Society, 23 October 2006, for serving as DATE Program Chair

- Recognition of Service Award for contributions to ACM as Program Chair of DATE 2006
- Best Paper Award DATE '99 for the paper "Symbolic functional vector generation for VHDL specifications" ACM/IEEE DATE '99 – Design, Automation and Test in Europe
- Best Paper Award at the conference IEEE/ACM Eurodac with EuroVHDL '96 for the paper "BDD-based Testability Estimation of VHDL Designs"
- Best Paper Award at the conference IEEE ICCD '98
- IEEE Award from the Northern Italy section as best student in 1984
- Award as the youngest graduate at Politecnico di Milano in 1984

Participation in international evaluation boards

- Member of the evaluation panel of the Department of Information technology and Electronic Engineering of ETH Zurich, 2021
- Participation to the evaluation board of ETH Zurich for the position of Full professor and Assistant Professor in Embedded Systems, 2020
- Participation to the PhD board for the University of Queensland, New Zealand, TIMA, Grenoble (France), University of Nice (France), University of Tampere (Finland), University of Amsterdam (The Netherlands), EPFL (Swiss).
- Member of the PhD Board in Information Engineering, Politecnico di Milano, 2010-2015 and member of the PhD Board in Data Science, Università di Bologna 2017 - 2020
- Participation to the evaluation board of EUROTECH Postdoc evaluation board 2018-2021
- Participation to the evaluation board for Assistant Professor selection, TU Wien, 2018
- Participation to the committee for the promotion to Professor in French University (Université de la Bretagne du Sud), University of Grenoble.
- Participation to the Promotion Committee to the position of Associate Professor of KTH Stockholm, Sweden
- Participation to the evaluation board for the French Government (AERES) of LabSTICC, a group of universities of Bretagne.
- California Board of Evaluation for full professor career evaluation (UCSD)
- Purdue University associate professor evaluation
- Michigan University full professor evaluation
- National University of Singapore full professor evaluation
- Participation to the Swedish Research Council research projects reviews 2013-2015
- Participation as evaluator in the Canadian NSF program, New Zealand national research program, Belgian research program, Israel Science Foundation.
- Member designated by the Ministry of Education and Research for the selection of the President of the Agenzia Spaziale Italiana 2014

- Member of the selection committee of the IEEE Transaction on Computers Journal 2014 and 2018

Publications record

She has co-authored more than 100 papers in international journals, 1 international research book, 20 chapters in international research books and more than 330 papers at international conferences. She holds 3 international patents on processor architecture features with STMicroelectronics and one national and European patent filed with Politecnico di Milano on indoor localization methodologies.

Teaching activities

She has taught courses in the area of Computer Architectures and Information Systems since her graduation first as teaching assistant at Politecnico di Milano and University of Colorado Boulder, then as professor at Università di Brescia, Politecnico di Milano and University of Illinois Chicago and at Politecnico di Milano and international summer schools for PhD students on more advanced topics. She has taught a course on Blockchain and Cryptoassets for the past 3 academic years in a joint Master program on Cyber Risk Strategy and Governance between Bocconi University and Politecnico di Milano. She coordinates a course on Research Skills in the Doctoral School of Politecnico di Milano.

Dissemination activities and social engagement

She has given different keynote speeches on her research activities in the past twenty years at scientific international and national conferences and as invited speaker in international universities and associations. She speaks on research strategies and university strategies in different contexts, both at national and international level.

She has worked since 2010 to improve the gender gap in STEM disciplines, giving speeches in national and international events and participating in different activities to create awareness in society. She has been instrumental in setting up the Diversity&Inclusion policies and organizational units at Politecnico di Milano to make the university an inclusive place to study and work, through the project POP (<https://pop.polimi.it>) in order to provide services that are inclusive to all.